

M.Tech Semester II (Communication & Signal Processing) (FN: LH 14, AF: LH 7)

Day	9:00am -10:00am	10:00am -11:00am	11:00am -12:00am	12:00am -2:00pm	2:00pm-3:00pm	3:00pm- 4:00pm	4:00pm -5:00pm
M				LUNCH	ITC(EC503)/JK	SPA(EC507)/KP	FOC(EC557)/SJ
T					AAD(EC501)/KM	ITC(EC503)/JK	FOC(EC557)/SJ
W	Library	ITC(EC503)/JK	SPA(EC507)/KP		FOC(EC557)/SJ	MWC(EC505)/TC	SPA(EC507)/KP
T	AM-LAB(EC515)/KM (Ph Robin)				AAD(EC501)/KM	MWC(EC505)/TC	
F					MWC(EC505)/TC	AAD(EC501)/KM	

M.Tech Semester II (VLSI) (Room: LH 8)

Day	9:00am -10:00am	10:00am -11:00am	11:00am - 12:00am	12:00am - 2:00pm	2:00pm-3:00pm	3:00pm- 4:00pm	4:00pm -5:00pm
M	VL511/MK(Kh Dayananda)			LUNCH	(ADD)VL501/MK	(ESD)VL503/RN	(SDM(m))VL505/NGC
T					(SDM(m))VL505/NGC	(SDM(m))VL505/NGC	(DIC)VL535/HTK
W					(DIC)VL535/HTK	(ESD)VL503/RN	(VSD)VL507/NGC
T					(VSD)VL507/NGC	(ADD)VL501/MK	(ESD)VL503/RN
F					((ADD)VL501/MK	(DIC)VL535/HTK	(VSD)VL507/NGC



**Prof. A. Dinamani Singh
HOD, ECE Deptt.**